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JCS8Y
U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10063100	FILING DATE 03/20/2002	CLASS 361	SUBCLASS 93.2	GAU 2838	EXAMINER H. J. H. K. Patel
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**APPLICANTS: Fletcher David; Lavoie Gregory;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	41PR09032
Verified and Acknowledged Examiners's initials		
TITLE : Methods and devices for optimizing power consumption of trip units containing microprocessors		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Fig.	
TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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